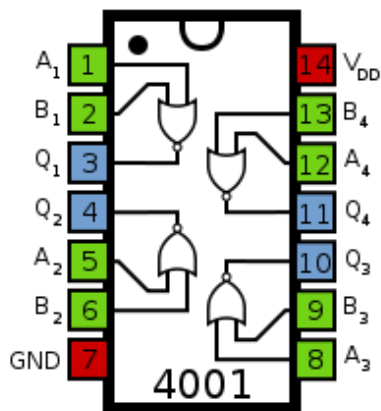
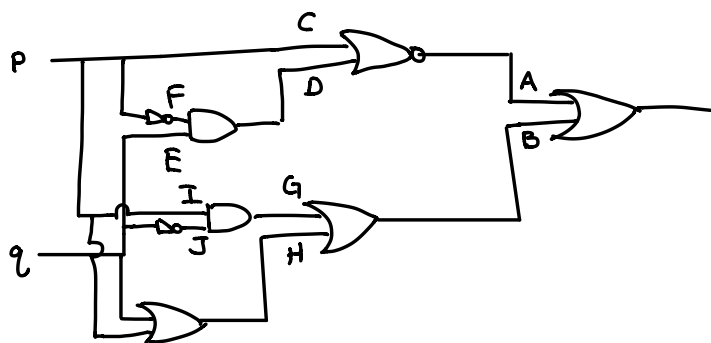
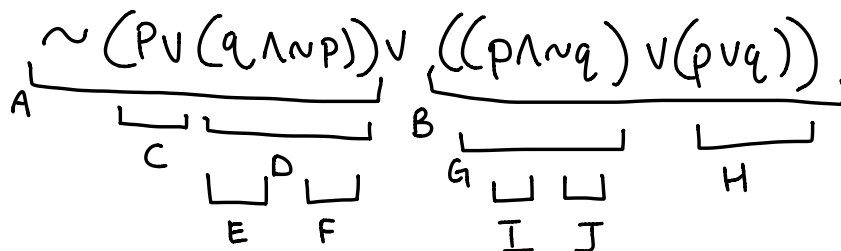


Transistor - Voltage Controller Switch

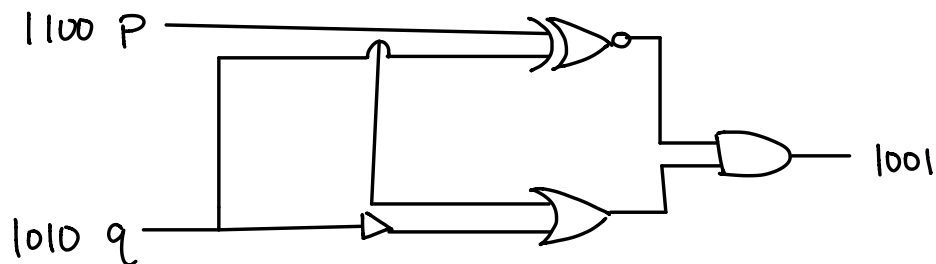


Pin out IC



$$1) (P \otimes q) \wedge (\sim P \vee q)$$

$$2) P \rightarrow (P \vee q)$$



$$3) (\sim(P \oplus q)) \wedge (\sim(P \wedge q) \wedge (P \vee q))$$

$$4) \sim p \wedge (q \vee \sim(p \wedge (p \vee q)))$$

